

# Austin Dibble

SOFTWARE ENGINEER · MASTER'S STUDENT

Glasgow, UK

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## Education

### University of Strathclyde

M.Sc. ADVANCED COMPUTER SCIENCE WITH ARTIFICIAL INTELLIGENCE

Glasgow, UK

Sept. 2022 - Sept. 2023

- **Relevant Coursework:** Deep Learning for Visual Computing, Statistics & Bayesian Analysis, Artificial Planning.

### Oregon State University

B.Sc. IN ELECTRICAL AND COMPUTER ENGINEERING

Corvallis, OR, USA

Sept. 2015 - Mar. 2020

- **Cumulative GPA:** 3.93 / 4.0 (UK 1st class equiv.).
- **Minor:** Spanish.

## Work Experience

### Siemens Digital Industries Software

SOFTWARE ENGINEER

Wilsonville, Oregon, USA

Apr. 2020 - Present

- Refining performance testing and improvements on Linux systems for Siemens' EDA product, improving process runtime by 40%.
- Operating within a cross-cultural and international team to coordinate bug fixes and performance improvements for customers around the world.
- Developing new UI features in a C++ environment, while utilizing the newest compiler and language features.
- Cooperating across departments to develop new features and performance improvements that meet the needs of a diverse customer base.

### Semiconductor Research Corporation

UNDERGRADUATE RESEARCHER

Corvallis, Oregon, USA

Oct. 2019 - Mar. 2020

- Collaborated with postgraduate students in digital signal processing for analog to digital converters.
- Investigated novel algorithms for improving the performance of A-to-D converters when operating in noisy and non-linear environments.
- Designed MATLAB simulations to analyze the performance of digital signal processing.

### IND LLC

EMBEDDED SYSTEMS ENGINEER

Vancouver, Washington, USA

Jun. 2019 - Apr. 2020

- Innovated firmware for ARM MCU to communicate and control physical processes such as temperature, humidity, pressure, and fluid flow via Internet communication.
- Managed the design and development of a new touch-screen display interface that featured an ARM Cortex-M7 processor, Bluetooth LE, Wi-Fi, and GPS.
- Coordinated with the hardware team, designing new schematics to meet customer requirements.
- Created firmware in C/C++ and an embedded GUI using uGFX.

### IND LLC

SOFTWARE ENGINEERING INTERN

Vancouver, Washington, USA

Jun. - Aug. 2017, Jun. - Aug. 2018

- Pioneered a GUI in Java 8 (JavaFX) for automating on-site server organization of employee data.
- Resolved software issues and communicated solutions to customers remotely.
- Initiated development of new control hardware, using 32-bit ARM Cortex-M4 processors with an RTOS (real-time operating system).
- Utilized processor datasheets to write new peripheral libraries, resulting in 30% increased efficiency.
- Formed and documented a new development toolchain for embedded C firmware.

## Honors & Awards

### UNIVERSITY OF STRATHCLYDE

- 2022 **Faculty of Science Masters Scholarship for International Students:** Scholarship awarded on a competitive basis to candidates who demonstrate excellent academic performance.

### OREGON STATE UNIVERSITY

- 2019 **Orin F. Zimmerman Scholarship:** Awarded to a single student on the basis of aptitude and interest in fostering environmentally acceptable and socially productive knowledge, innovations, and applications in electrical technology.
- 2018 **Knaus Electrical Engineering Scholarship:** Granted to one student on basis of GPA from among the top 10% of their class.